

Appl. No. 10/710,905
Amdt. dated March 15, 2007
Reply to Office action of December 20, 2006

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

5 **Listing of Claims:**

claims 1-17 (canceled)

18. (New) An integrated circuit design method, comprising steps of:

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providing a plurality of kinds of description instructions and a database specific to an integrated circuit;

15 optionally modifying one kind of the description instructions; and

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automatically enabling the updating of the database once any of the description instructions is modified;

20 wherein other kinds of description instructions are automatically updated along with

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the updating of the database.

19. (New) The integrated circuit design method of claim 18, wherein the integrated circuit is an application-specific integrated circuit (ASIC).

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20. (New) The integrated circuit design method of claim 18, wherein the description instruction being optionally modified is a timing slack report of the integrated circuit.

21. (New) The integrated circuit design method of claim 20, wherein the description instructions being automatically updated include a netlist, a noise analysis report and/or a power analysis report of the integrated circuit.
- 5 22. (New) The integrated circuit design method of claim 18, wherein the description instruction being optionally modified is a netlist of the integrated circuit.
- 10 23. (New) The integrated circuit design method of claim 22, wherein the description instructions being automatically updated include a timing slack report, a noise analysis report and/or a power analysis report of the integrated circuit.
- 15 24. (New) The integrated circuit design method of claim 18, wherein the description instruction being optionally modified is a noise analysis report of the integrated circuit.
- 20 25. (New) The integrated circuit design method of claim 24, wherein the description instructions being automatically updated include a timing slack report, a netlist and/or a power analysis report of the integrated circuit.
- 25 26. (New) The integrated circuit design method of claim 18, wherein the description instruction being optionally modified is a power analysis report of the integrated circuit.
27. (New) The integrated circuit design method of claim 26, wherein the description instructions being automatically updated include a timing slack report, a netlist and/or a noise analysis report of the integrated circuit.